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S J M INSTITUTE OF TECHNOLOGY

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NH-4 Bypass, P.B.No:73, CHITRADURGA -577502, Karnataka State.

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Display of Department of CS& E Sample POs, PSOs & COs

SJM VIDYAPEETHA (R)
SJM INSTITUTE OF TECHNOLOGY, CHITRADURGA-577502
DEPARTMENT OF
COMPUTER SCIENCE & ENGINEERING

PROGRAM OUTCOMES (POs)

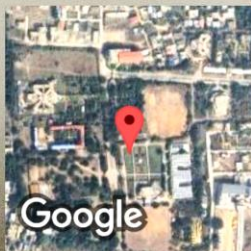
- 1. Engineering knowledge:** Apply the knowledge of mathematics, science, engineering fundamentals, and an engineering specialization to the solution of complex engineering problems.
- 2. Problem analysis:** Identify, formulate, review research literature, and analyze complex engineering problems reaching substantiated conclusions using first principles of mathematics, natural sciences, and engineering sciences.
- 3. Design/development of solutions:** Design solutions for complex engineering problems and design system components or processes that meet the specified needs with appropriate consideration for the public health and safety, and the cultural, societal, and environmental considerations.
- 4. Conduct investigations of complex problems:** Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions.
- 5. Modern tool usage:** Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modeling to complex engineering activities with an understanding of the limitations.
- 6. The engineer and society:** Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to the professional engineering practice.
- 7. Environment and sustainability:** Understand the impact of the professional engineering solutions in societal and environmental contexts, and demonstrate the knowledge of, and need for sustainable development.
- 8. Ethics:** Apply ethical principles and commit to professional ethics and responsibilities and norms of the engineering practice.
- 9. Individual and team work:** Function effectively as an individual, and as a member or leader in diverse teams, and in multidisciplinary settings.
- 10. Communication:** Communicate effectively on complex engineering activities with the engineering community and with society at large, such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions.
- 11. Project management and finance:** Demonstrate knowledge and understanding of the engineering and management principles and apply these to one's own work, as a member and leader in a team, to manage projects and in multidisciplinary environments.
- 12. Life-long learning:** Recognize the need for, and have the preparation and ability to engage in independent and lifelong learning in the broadest context of technological change.

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DEPARTMENT OF
COMPUTER SCIENCE & ENGINEERING

PROGRAM SPECIFIC OUTCOMES (PSOs)

PSO1: An ability to design and develop hardware and software in emerging technology environments like cloud computing, embedded products and real-time systems. (Orientation towards Systems Programming)

PSO2: Knowledge of data management system like data acquisition, big data so as to enable students in solving problems using the techniques of data analytics like pattern recognition and knowledge discovery. (Orientation towards Data Sciences)



Google



GPS Map Camera

Chitradurga, Karnataka, India
69qp+v6h Sjmcp Medicinal Garden, Bmch Rd, Hanumantha
Nagara, Chitradurga, Karnataka 577502, India
Lat 14.238972° Long 76.385628°
15/01/25 03:00 PM GMT +05:30


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Display of Department of E & C Sample POs, PSOs & COs

SJM INSTITUTE OF TECHNOLOGY, CHITRADURGA-577502
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

PROGRAM SPECIFIC OUTCOME: PSOs

The students will be able to –

- PSO.1 Analyze and design electronic systems for signal processing and communication applications.
- PSO.2 Demonstrate the conceptual domain knowledge with respect to architecture, design, analysis and engineering deployment in data communication and computer networking, embedded system, microcontroller and advanced communication system.
- PSO.3 Identify and apply domain specific tools for design, analysis, synthesis and validation of VLSI, optical fiber communication and communication systems.

SJM INSTITUTE OF TECHNOLOGY, CHITRADURGA-577502
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

PROGRAM OUTCOMES (POs)

1. **Engineering knowledge:** Apply the knowledge of mathematics, science, engineering fundamentals, and an engineering specialization to the solution of complex engineering problems.
2. **Problem analysis:** Identify, formulate, review research literature, and analyze complex engineering problems reaching substantiated conclusions using first principles of mathematics, natural sciences, and engineering sciences.
3. **Design/development of solutions:** Design solutions for complex engineering problems and design system components or processes that meet the specified needs with appropriate consideration for the public health and safety, and the cultural, societal, and environmental considerations.
4. **Conduct investigations of complex problems:** Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions.
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12. **Life-long learning:** Recognize the need for, and have the preparation and ability to engage in independent and lifelong learning in the broadest context of technological change.



Google

GPS Map C

Chitradurga, Karnataka, India
 69qp+v6h Sjmcp Medicinal Garden, Bmch Rd, Hanumantha
 Nagara, Chitradurga, Karnataka 577502, India
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Display of Department of E & E Sample POs, PSOs & COs

SJM VIDYAPEETHA (R)
SJM INSTITUTE OF TECHNOLOGY, CHITRADURGA-577502
DEPARTMENT OF
ELECTRICAL & ELECTRONICS ENGINEERING

PROGRAM OUTCOMES (POs)

- Engineering knowledge:** Apply the knowledge of mathematics, science, engineering fundamentals, and an engineering specialization to the solution of complex engineering problems.
- Problem analysis:** Identify, formulate, review research literature, and analyze complex engineering problems reaching substantiated conclusions using first principles of mathematics, natural sciences, and engineering sciences.
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SJM VIDYAPEETHA (R)
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DEPARTMENT OF
ELECTRICAL & ELECTRONICS ENGINEERING

Program Specific Outcomes PSOs

PSO1: Ability to specify architect, design and analyze systems that efficiency generate, transmit, distribute and utilize electrical power.

PSO2: Ability to specify design, prototype and test modern electronic systems that perform analog and digital processing function.

PSO3: Ability to use software for design, simulation and analysis of electrical system.



GPS Map Camera

Chitradurga, Karnataka, India
Electrical Dept., Hanumantha Nagara, Chitradurga, Karnataka
577502, India
Lat 14.23779° Long 76.386502°
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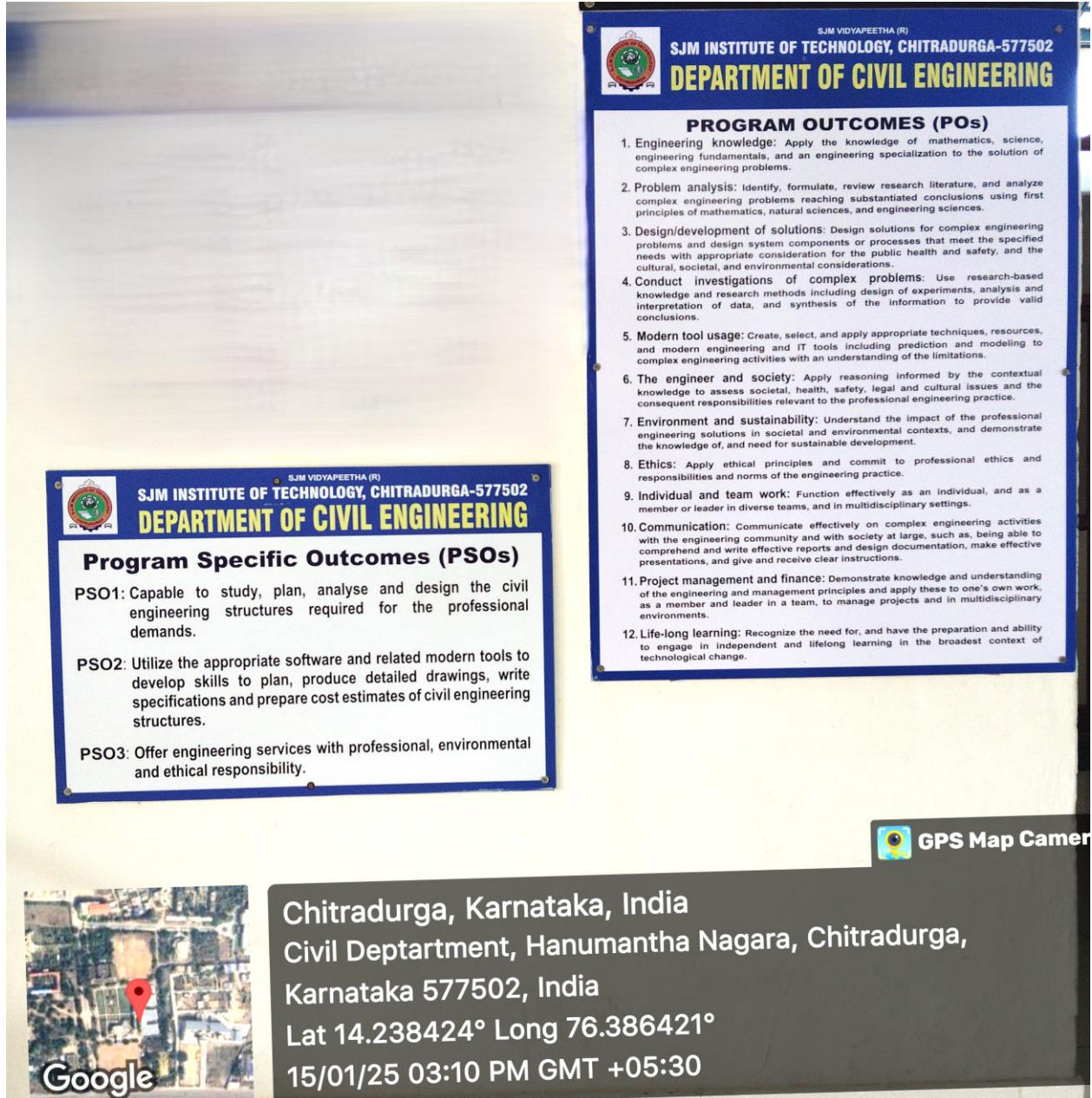
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Display of Department of Civil Engineering Sample POs, PSOs & COs





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Display of Department of Mechanical Engineering Sample POs, PSOs & COs

SJM VIDYAPEETHA (R)
SJM INSTITUTE OF TECHNOLOGY, CHITRADURGA-577502
DEPT. MECHANICAL ENGINEERING

PROGRAM OUTCOMES (POs)

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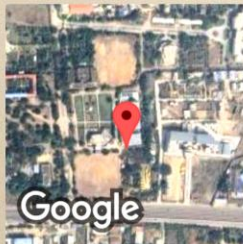
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DEPT. OF MECHANICAL ENGINEERING

PROGRAM SPECIFIC OUTCOMES (PSOs)

PSO1: Apply their knowledge in the domain of engineering mechanics, thermal and fluid sciences to solve engineering problems utilizing advanced technology.

PSO2: Successfully apply the principles of design, analysis and implementation of mechanical systems / processes which have been learned as a part of the curriculum.

PSO3: Develop and implement new ideas on product design and development with the help of modern CAD/CAM tools, while ensuring best manufacturing practices



Chitradurga, Karnataka, India
Sjmit Quarters, Kodana Hatti Rd, Hanumantha Nagara,
Chitradurga, Karnataka 577502, India
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Display of POs, PSOs & COs in Laboratory Manuals

VISVESVARAYA TECHNOLOGICAL UNIVERSITY

JNANA SANGAMA, BELGAVI -590 014



Lab Manual (CSE STREAM)

"PRINCIPLES OF PROGRAMMING USING C "

INTEGRATED

(Theory / Practical)

(Effective from the academic Year 2022-2023)

SEMESTER – I / II

Subject Code: BPOPS103/203

(Choice Based Credit System)

Prof. Poral Nagaraj B.E., M.Tech.

Asso Professor, Dept. of CS&E,

S.J.M.I.T, CHITRADURGA.



DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

SJM Vidyapeetha®

Sri Jagadguru Mallikarjuna Murugharajendra

Institute of Technology

CHITRADURGA -577 502


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SJM Institute of Technology, Chitradurga - 577502

Department of Computer Science & Engineering

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Program Specific Outcomes (PSOs)

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**VTU curriculum syllabus sample copy providing COs**

Digital Design and Computer Organization		Semester	3
Course Code	BCS302	CIE Marks	50
Teaching Hours/Week (L:T:P: S)	3:0:2:0	SEE Marks	50
Total Hours of Pedagogy	40 hours Theory + 20 Hours of Practicals	Total Marks	100
Credits	04	Exam Hours	3
Examination nature (SEE)	Theory		
Course objectives:			
<ul style="list-style-type: none"> To demonstrate the functionalities of binary logic system To explain the working of combinational and sequential logic system To realize the basic structure of computer system To illustrate the working of I/O operations and processing unit 			
Teaching-Learning Process (General Instructions)			
These are sample Strategies; that teachers can use to accelerate the attainment of the various course outcomes.			
<ol style="list-style-type: none"> Chalk and Talk Live Demo with experiments Power point presentation 			
MODULE-1		8 Hr	
Introduction to Digital Design: Binary Logic, Basic Theorems And Properties Of Boolean Algebra, Boolean Functions, Digital Logic Gates, Introduction, The Map Method, Four-Variable Map, Don't-Care Conditions, NAND and NOR Implementation, Other Hardware Description Language – Verilog Model of a simple circuit.			
Text book 1: 1.9, 2.4, 2.5, 2.8, 3.1, 3.2, 3.3, 3.5, 3.6, 3.9			
MODULE-2		8 Hr	
Combinational Logic: Introduction, Combinational Circuits, Design Procedure, Binary Adder- Subtractor, Decoders, Encoders, Multiplexers. HDL Models of Combinational Circuits – Adder, Multiplexer, Encoder. Sequential Logic: Introduction, Sequential Circuits, Storage Elements: Latches, Flip-Flops.			
Text book 1: 4.1, 4.2, 4.4, 4.5, 4.9, 4.10, 4.11, 4.12, 5.1, 5.2, 5.3, 5.4.			
MODULE-3		8 Hr	
Basic Structure of Computers: Functional Units, Basic Operational Concepts, Bus structure, Performance – Processor Clock, Basic Performance Equation, Clock Rate, Performance Measurement. Machine Instructions and Programs: Memory Location and Addresses, Memory Operations, Instruction and Instruction sequencing, Addressing Modes.			
Text book 2: 1.2, 1.3, 1.4, 1.6, 2.2, 2.3, 2.4, 2.5			
MODULE-4		8 Hr	
Input/output Organization: Accessing I/O Devices, Interrupts – Interrupt Hardware, Enabling and Disabling Interrupts, Handling Multiple Devices, Direct Memory Access: Bus Arbitration, Speed, size and Cost of memory systems. Cache Memories – Mapping Functions.			
Text book 2: 4.1, 4.2.1, 4.2.2, 4.2.3, 4.4, 5.4, 5.5.1			
MODULE-5		8 Hr	



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Basic Processing Unit: Some Fundamental Concepts: Register Transfers, Performing ALU operations, fetching a word from Memory, Storing a word in memory. Execution of a Complete Instruction. **Pipelining:** Basic concepts, Role of Cache memory, Pipeline Performance.

Text book 2: 7.1, 7.2, 8.1

PRACTICAL COMPONENT OF IPCC

SLN	Experiments
0	Simulation packages preferred: Multisim, Modelsim, PSpice or any other relevant
1	Given a 4-variable logic expression, simplify it using appropriate technique and simulate the same using basic gates.
2	Design a 4 bit full adder and subtractor and simulate the same using basic gates.
3	Design Verilog HDL to implement simple circuits using structural, Data flow and Behavioural model.
4	Design Verilog HDL to implement Binary Adder-Subtractor – Half and Full Adder, Half and Full Subtractor.
5	Design Verilog HDL to implement Decimal adder.
6	Design Verilog program to implement Different types of multiplexer like 2:1, 4:1 and 8:1.
7	Design Verilog program to implement types of De-Multiplexer.
8	Design Verilog program for implementing various types of Flip-Flops such as SR, JK and D.

Course outcomes (Course Skill Set):

At the end of the course, the student will be able to:

CO1: Apply the K-Map techniques to simplify various Boolean expressions.

CO2: Design different types of combinational and sequential circuits along with Verilog programs.

CO3: Describe the fundamentals of machine instructions, addressing modes and Processor performance.

CO4: Explain the approaches involved in achieving communication between processor and I/O devices.

CO5: Analyze internal Organization of Memory and Impact of cache/Pipelining on Processor Performance.

CO's


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Sample Internal Test question Papers with COs

Department of Computer Science & Engineering First Internal Assessment Question Paper (2023-24)

Name of the faculty: Prof. Poral Nagaraj					
Course Name: "Principles of Programming Using C"			Course Code: BPOPS103	Max. Marks: 25	
Semester: 1 st Sem.	Section: 'A'	Date: 31/10/2023	Time: 9:15 A.M to 10:15 A.M		
<i>Note : Answer two full questions</i>					
Q.No	Questions	Marks	CL	CO	PO
1.a	Define a computer. Explain how the computer components are organized?	6	U	1	PO1
1.b	Define a token in C language. Explain with example any three tokens used in C.	6.5	U	2	PO2
OR					
2.a	How would you distinguish primary memory from secondary memory?	5	U	1	PO1
2.b	Write the basic structure of a C program and explain with a relevant example.	7.5	U	2	PO2
OR					
3.a	Explain formatted input and output functions used in C with appropriate syntax and examples.	6	U	2	PO1, PO2
3.b	What is an identifier? List the rules for an identifier. Give two examples each for valid and invalid identifiers.	6.5	U	2	PO2
OR					
4.a	How would you explain basic data types used in C language with suitable examples?	8	U	2	PO2
4.b	Write a program to find largest of three numbers using conditional operator.	4.5	Ap	2	PO1, PO2, PO3

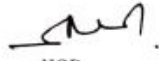
CL: Cognitive Level (R: Remember, U: Understand, Ap: Apply, A: Analyze, E: Evaluating, C: Creating)
CO: Course outcomes, PO: Program outcomes.

COURSE OUTCOMES

CO 1: Elucidate the basic architecture and functionalities of a computer and also recognize the hardware parts.
CO 2: Apply programming constructs of C language to solve the real world problem.
CO 3: Explore user-defined data structures like arrays in implementing solutions to problems like searching and sorting
CO 4: Explore user-defined data structures like structures, unions and pointers in implementing solutions.
CO 5: Design and Develop Solutions to problems using modular programming constructs using functions.

*****ALL THE BEST*****


Academic Coordinator


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SJMIT, Chitradurga


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Department of Computer Science & Engineering Second Internal Assessment Question Paper (2023-24)

Name of the faculty: Prof. Poral Nagaraj					
Course Name: "Principles of Programming Using C"			Course Code: BPOPS103	Max. Marks: 25	
Semester: 1 st Sem.	Section: 'A' & 'B'	Date: 04/11/2023	Time: 9:15 A.M to 10:15 A.M		
<i>Note : Answer two full questions</i>					
Q.No	Questions	Marks	CL	CO	PO
1.a	Explain Switch statement with an example.	7.5	U	2	PO1, PO2
1.b	Write a C program to find factorial of the given number.	5	Ap	2	PO1, PO2, PO3
OR					
2.a	Illustrate for loop with an example.	5	U	2	PO1, PO2
2.b	Explain elements of user defined functions.	7.5	U	5	PO1, PO2
OR					
3.a	Compare while loop with do while loop.	5	U	2	PO1, PO2
3.b	Write a C program to implement Bubble sorting technique.	7.5	Ap	3	PO1, PO2, PO3
OR					
4.a	Explain how one dimensional arrays are declared and initialized	5	U	3	PO1, PO2
4.b	Write a C program to multiply two matrices of order 2X2.	7.5	Ap	3	PO1, PO2, PO3


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CO: Course outcomes, PO: Program outcomes.

COURSE OUTCOMES

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Course Name: "Principles of Programming Using C"			Course Code: BPOPS103		Max. Marks: 25
Semester: 1 st Sem.	Section: 'A' & 'B'	Date: 11/01/2024	Time: 9:15 A.M to 10:15 A.M		
<i>Note : Answer two full questions</i>					
Q.No	Questions	Marks	CL	CO	PO
1.a	Describe the evolution of Computers by mentioning how computers in one generation are better than their predecessors.	7.5	U	1	PO1
1.b	List at least any five Input and Output devices each	5	R	1	PO1
OR					
2.a	Define a structure. How structures are defined, declared and initialized?	7.5	U	4	PO1, PO2
2.b	Mention various operations that can be performed on strings using built-in functions. Explain any two functions.	5	U	3	PO1, PO2
OR					
3.a	Write the difference between structure and unions.	5	U	4	PO1, PO2
3.b	Write a program to store the details (Name, Rollno and Marks) of n students using array of structures.	7.5	Ap	4	PO1, PO2, PO3
OR					
4.a	What is a pointer? Write a C program to implement Pointer arithmetic.	6	Ap	4	PO1, PO2, PO3
4.b	What are the different modes of opening a file? Briefly explain.	6.5	U	5	PO1, PO2

CL: Cognitive Level (R: Remember, U: Understand, Ap: Apply, A: Analyze, E: Evaluating, C: Creating)
CO: Course outcomes, PO: Program outcomes.

COURSE OUTCOMES

CO 1: Elucidate the basic architecture and functionalities of a computer and also recognize the hardware parts.
CO 2: Apply programming constructs of C language to solve the real world problem.
CO 3: Explore user-defined data structures like arrays in implementing solutions to problems like searching and sorting
CO 4: Explore user-defined data structures like structures, unions and pointers in implementing solutions.
CO 5: Design and Develop Solutions to problems using modular programming constructs using functions.


Academic Coordinator

*****ALL THE BEST*****


HOD
Department Of Computer Science
SJMIT, Chitradurga


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Sample of COs written in Internal Test Answer Book

Serial No B

		S.J.M. Vidyapeetha (R)								
Sri Jagadguru Mallikarjuna Murugharajendra Institute of Technology										
(Recognized by AICTE, New Delhi and Affiliated to Visvesvaraya Technological University, Belagavi)										
NAAC Accredited with "B++" Grade										
NH-4 Bypass, P. B. No: 73, CHITRADURGA - 577502, Karnataka State, INDIA.										
										
Year : 20²³ -20²⁴.										
Name	<u>Kusuma .T.S.</u>									
USN	<u>4</u>	<u>S</u>	<u>M</u>	<u>2</u>	<u>2</u>	<u>C</u>	<u>S</u>	<u>0</u>	<u>4</u>	<u>9</u>
Course	<u>BE</u>									
Branch	<u>Computer Science and Engineering.</u>									
Subject Name	<u>DBMS</u>									
Subject Code	<u>B</u>	<u>C</u>	<u>S</u>	<u>4</u>	<u>0</u>	<u>3</u>				
Semester / Section	<u>IV</u>	<u>B</u>								
Internal Assessment Record						Final Marks		<u>23</u>		
								<u>40 25</u>		
(For Institute's Internal Use Only)										


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COURSE OUTCOME'S (COs)

CO1:- Describe the basic elements of a relational database management system.

CO2:- Design entity relationship for the given scenario.

CO3:- Apply various structured Query language (SQL) statements for database manipulation.

CO4:- Analyze various normalization forms for the given application.

CO5:- Develop database applications for the given real world problem.

CO6:- understand the concepts related to NOSQL database.



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Sample of COs written in Laboratory Record Book

S.J.M. Vidyapeetha (Regd)

**Sri Jagadguru Mallikarjuna Murugharajendra
Institute of Technology**

(Affiliated To Visveshwaraiah Technological University Belagavi, Recognised By AICTE, New Delhi & Approved By Govt of Karnataka)
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P.B. No. 73, Chitradurga - 577 502, Karnataka State, INDIA



PRACTICAL RECORD BOOK

Name : Sirisha. D

U.S. No. : 4SM2306092

Subject & Code : Digital Design & Computer Organization (lab)
[BCS302]

Semester / Year : 3rd, 2nd year


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Course outcomes :-

CO1: Apply the K-map techniques to simplify various boolean expressions.

CO2: design different type of combinational and sequential circuits along with verilog programs.

CO3: describe the fundamentals of machine instructions, addressing modes & processor performance.

CO4: explain the approaches involved in achieving communication between processors and I/O devices.

CO5: Analyze internal organization of memory and impact of the cache | pipelining on processor performance.


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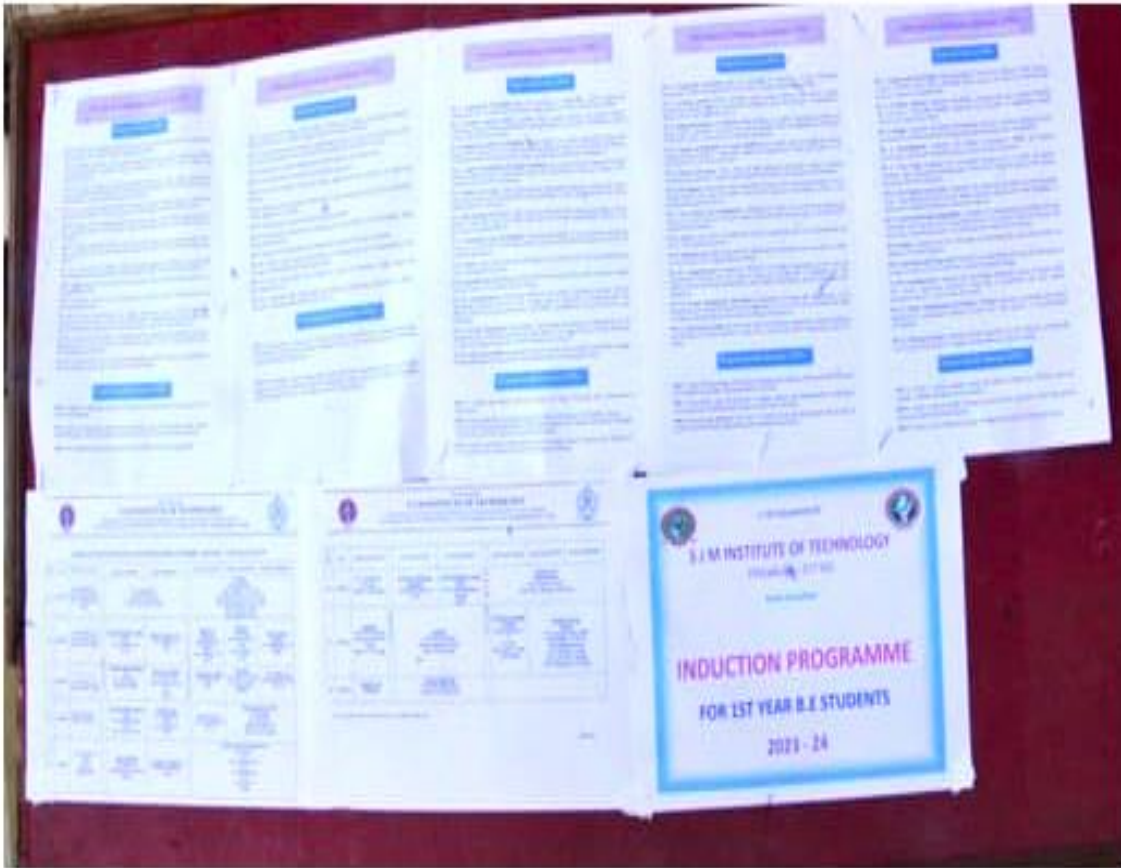
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POs and PSOs displayed during INDUCTION Program




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