Important Dates

Registration date: Till morning 19.08.2022

Online registration

www.sjmit.ac.in

Registration Fee

Registration fee: Rs. 300/- for Students and Rs. 500/- for Faculty.

Participants can pay the fee either by demand draft or by cash (at the time of registration).Demand Draft should drawn be in the favor of "Principal, SJMIT, Chitradurga".

Programme Schedule

Date: 19th August 2022

Timing	Event	Venue
9 AM to 9.30 AM	Registration	SIR M V Hall
9.30 AM to 10 AM	Inauguration	SIR M V Hall
10 AM to 1 PM	Demonstration	SIR M V Hall
1 PM to 2 PM	Lunch	SIR M V Hall
2 PM to 4.30 PM	Hands on Session	CSE Lab
4.30 PM to 5.30 PM	Cultural Programme	SIR M V Hall

For more information: www.sjmit.ac.in

SJMIT & Dept. Of CSE

S.J.M Institute of Technology (SJMIT) was established in the academic year 1980-81 with the blessings Sri Sri Sri Mallikarjuna Murugharajendra Mahaswamiji, with an intention to take higher education to the threshold of aspirants. Dr. Shivamurthy Murugha Sharanaru is the present Pontiff of Sri Murugha Math and the President of SJM Vidyapeetha. SJMIT is located just adjacent to the Pune-Bangalore National Highway-4, the institute is well connected with road and railway network and just 200 kms drive from Bangalore. The eco-friendly campus filled with flora-n-fauna sprawled with lush green garden spread over vast 22 acres of land makes an ideal platform for higher education center with homely atmosphere.

The Computer Science and Engineering Department established in the year 1983 is one of the largest premier departments having state-of-the-art infrastructure facility. It imparts an outstanding educational opportunity for those planning to pursue a career or to gain in-depth knowledge in computing technology.

M.Tech in Computer Science and Engineering course was added to department in the academic year 2011-12 with an intake of 18 students with a view to build a vibrant learning environment through value based academic principles where in all involved shall contribute effectively and efficiently with all responsibility to prepare students to meet the global challenges related to IT world.

Program Coordinators:
Prof. Ramesh BE
Dr. Krishnareddy K R

Technical Coordinators:
Dr. Aravinda T V
Prof. Kumarswamy H

Head of the Department:
Prof. Poral Nagaraj

IQAC Coordinator:
Dr. Jagannatha N

Workshop

On

"Microcontroller & Embedded System"

Dr. Bharath P B
Principal

Organized by:

Department of Computer Science & Engineering,
IQAC, SJMIT

SJM Vidyapeetha (R)
SJM INSTITUTE OF TECHNOLOGY
(Affiliated to VTU, Belagavi and recognized by AICTE, New Delhi)
"Accredited by NAAC with B++ Grade"
NH-4 Bypass, Post Box Number: 73
Chitradurga – 577 502
www.sjmit.ac.in
Phone: 08194 – 222866

All are cordially invited by The Management, Principal, HOD and Staff Members

About the Program

The aim of the workshop is to provide a platform for enhancing knowledge about the embedded system design with microcontrollers. The topics for the workshop include practical aspects of programming and debugging a microcontroller in real-time environment. The platforms to be used in the workshop are 8051 based microcontrollers and Arduino. It also includes basics of FPGA based designing for applications.

ARM Processor

ARM processor features include:

- Load/store architecture.
- An orthogonal instruction set.
- Mostly single-cycle execution.
- Enhanced power-saving design.
- 64 and 32-bit execution states for scalable high performance.
- Hardware virtualization support.

Objectives

- To provide basics of embedded system
- To provide idea of FPGA based implementation

Workshop Content

- Introduction to Embedded Systems
- Basics of Microcontrollers
- Embedded System development boards
- Interfacing with different sensors,
- Interfacing with DC & Servo motor
- Chip Scope Analyzer

Speakers

Dr. Sanjeev Kunte, Prof., & HOD, JNNCE, Shivamogga

Mr. Chidambar Kulkarni, Zonal Manager, TMI Systems, Bengaluru

Who should Attend

Students and faculty members of VTU affiliated engineering colleges.

Duration and Venue

The workshop will be held at SIR M V Hall, SJMIT, Chitradurga on 19th August 2022.

Contact Details

HOD & Coordinators,

Dept. of CSE, SJMIT, Chitradurga,

Email: hod.cse@sjmit.ac.in ,

Mobile: 7676346066,

8310916469, 9964347941, 9353533044

Registration Form

Name:

USN / Designation:

Mobile number:

Email:

Payment Details:

Name of the Bank:

Amount:

DD Number:

Date:

Signature of the Applicant: